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Cheng et al.

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(54) COMPILE TIME POINTER ANALYSIS
ALGORITHM STATEMENT OF
GOVERNMENT INTEREST

Publication Classification

(51) Int. Cl.⁷ G06F 9/44
(52) U.S. Cl. 717/4(76) Inventors: Ben-Chung Cheng, Milpitas, CA (US);
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(57) ABSTRACT

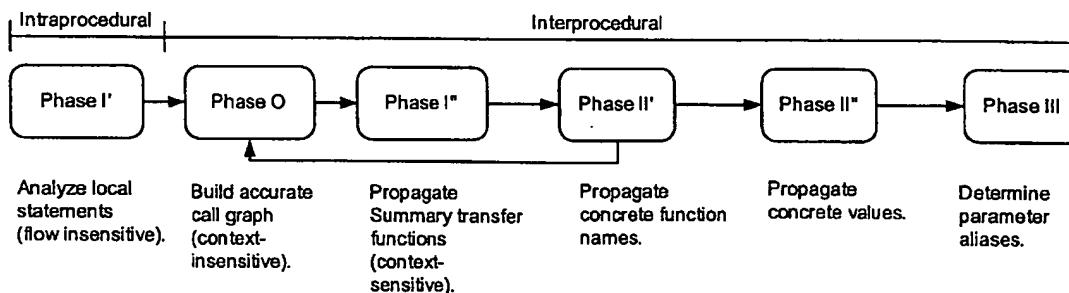
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In compiling a program, the present algorithm first analyzes each function in the program as an isolated compilation unit where parameters and global variables are temporarily assumed to have uninitialized values. This stage of the algorithm, the intraprocedural phase, will summarize the intraprocedural behavior of a function in a flow-insensitive manner, including how it can affect memory accesses in the caller and callee functions, and how its memory accesses can be affected by the caller and callee functions. The summarized behavior of each function is the only information to be processed in the next stage, the interprocedural stage. A significant size reduction is achieved in the summarized representation as compared to the full function body. This facilitates aggressive optimization of even large programs.

(21) Appl. No.: 09/770,029

(22) Filed: Jan. 25, 2001

Related U.S. Application Data

(63) Non-provisional or provisional application No.
60/182,769, filed on Feb. 16, 2000.

0089 what when where } to propagate

iteratively propagate
- points-to-relations
- func names
- concrete values



US 20030172135A1

(19) United States

(12) Patent Application Publication
Bobick et al.(10) Pub. No.: US 2003/0172135 A1
(43) Pub. Date: Sep. 11, 2003(54) SYSTEM, METHOD, AND DATA
STRUCTURE FOR PACKAGING ASSETS
FOR PROCESSING AND DISTRIBUTION ON
MULTI-TIERED NETWORKS

179, filed on Oct. 2, 2000, now abandoned. Provisional application No. 60/254,377, filed on Dec. 8, 2000. Provisional application No. 60/262,288, filed on Jan. 17, 2001.

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Publication Classification

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G06F 9/44
(52) U.S. Cl. 709/220; 709/201; 709/315Correspondence Address:
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(57) ABSTRACT

The present invention provides a system, method, and data structure for packaging assets for processing and distribution over a multi-tiered network. An asset may represent network and/or application components (e.g., data, objects, applications, program modules, etc.) that may be distributed among the various resources of the network. In an embodiment, the package structure includes at least one representation of an asset having a logic/data portion and an asset extended environment portion, and a package extended environment that includes package information associated with at least one asset.

(21) Appl. No.: 09/947,162

(22) Filed: Sep. 4, 2001

Related U.S. Application Data

(60) Provisional application No. 60/229,685, filed on Sep. 1, 2000. Provisional application No. 60/236,864, filed on Sep. 29, 2000. Provisional application No. 60/237,

1450

Package ID 1414	Package Timing 1450						Location 1420	Other 1463
	Immediate 1452	Delivery Start Time 1454	Delivery End Time 1456	Expire Time 1458	Remove Time 1460	Refresh Rate 1462		

1455

synchronization and is propagated

Package Definition Data Structure



US005671419A

United States Patent [19]
Carini et al.

[11] Patent Number: 5,671,419
[45] Date of Patent: Sep. 23, 1997

[54] INTERPROCEDURAL DATA-FLOW ANALYSIS THAT SUPPORTS RECURSION WHILE ONLY PERFORMING ONE FLOW-SENSITIVE ANALYSIS OF EACH PROCEDURE

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[73] Assignee: International Business Machines Corporation, Armonk, N.Y.

[21] Appl. No.: 490,879

[22] Filed: Jun. 15, 1995

[51] Int. Cl.⁶ G06F 9/44

[52] U.S. Cl. 395/709

[58] Field of Search 395/709

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Primary Examiner—Kevin A. Kriess

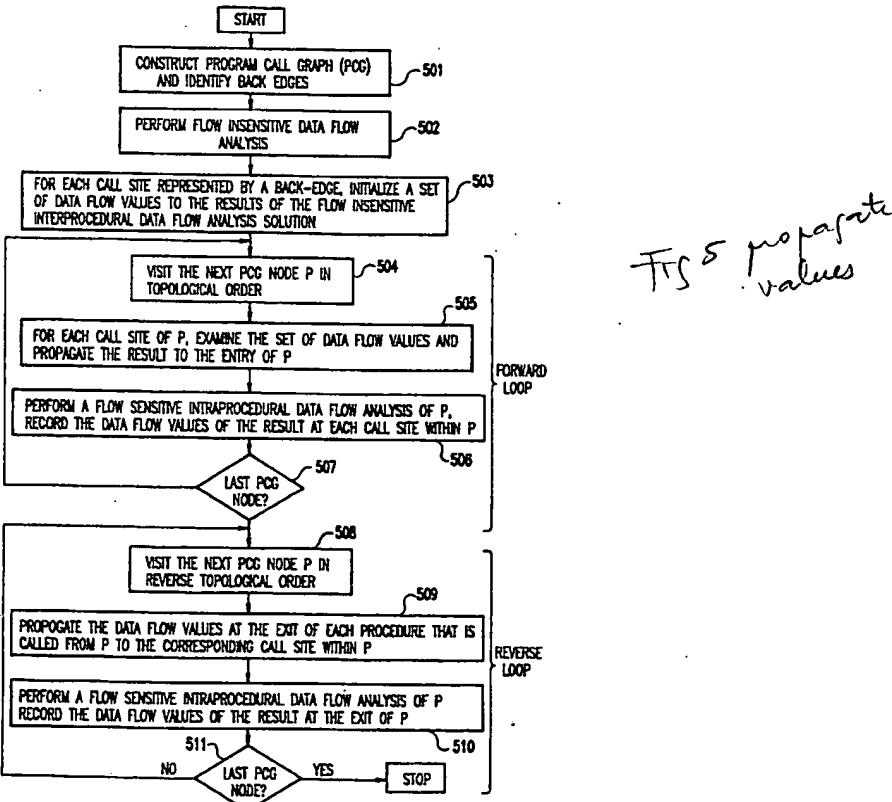
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[57] ABSTRACT

A computer implemented method performs flow-sensitive interprocedural data flow analysis without iteration for a class of interprocedural problems. The accuracy of the solution can approach the iterative result without the compile time cost. For interprocedural constant propagation (ICP), this method is more effective than existing methods and costs about the same compilation time. For flow-sensitive ICP over a program call graph (PCG), the method supports recursion while only performing one flow-sensitive analysis of each routine. If the PCG has cycles, a flow-insensitive solution is precomputed for constant propagation. During the flow-sensitive computation, the flow-insensitive result is used for a back edge. This permits a flow-sensitive solution to be obtained in one forward traversal of the PCG. This method can also be used to compute returned constants with one reverse traversal of the PCG. For flow-sensitive USE over a program call graph (PCG), the method supports recursion while only performing one flow-sensitive analysis of each routine. If the PCG has cycles, a flow-insensitive solution for a reference set (REF) is precomputed. During the flow-sensitive USE computation, the flow-insensitive REF solution is used for a back edge. This permits a flow-sensitive USE solution to be obtained in one reverse traversal of the PCG.

5 Claims, 8 Drawing Sheets





US006546551B1

(12) **United States Patent**
Sweeney et al.

(10) **Patent No.:** US 6,546,551 B1
(45) **Date of Patent:** Apr. 8, 2003

(54) **METHOD FOR ACCURATELY EXTRACTING LIBRARY-BASED OBJECT-ORIENTED APPLICATIONS**

(75) Inventors: Peter Francis Sweeney, Spring Valley, NY (US); Frank Tip, Mount Kisco, NY (US)

(73) Assignee: International Business Machines Corporation, Armonk, NY (US)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: 09/408,224

(22) Filed: Sep. 28, 1999

(51) Int. Cl.⁷ G06F 9/45

(52) U.S. Cl. 717/154; 717/153; 717/148; 717/165; 717/156

(58) Field of Search 717/151, 108, 717/116, 131, 132, 133, 154, 155, 153, 56, 157, 128, 109, 113, 104, 148, 165, 156; 707/1, 10, 103 R; 711/171; 345/594

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(57)

ABSTRACT

The present invention is capable of accurately extracting multiple applications with respect to a class library. The invention relies on a configuration file for an application program and/or library, which describes how program components in the program/library should be preserved under specified conditions. The invention may be used in application extraction tools, and in tools that aim at enhancing performance using whole-program optimizations. The invention may be used as an optimization to reduce application size by eliminating unreachable methods. In the alternative, the invention may be used as a basis for optimizations that reduce execution time (e.g., by means of call devirtualization), and as a basis for tools for program understanding and debugging.

20 Claims, 4 Drawing Sheets

